

JC 872 U.S. PTO
09/000946

Class	Subclass	Issue Classification

PATENT NUMBER.

U.S. UTILITY Patent Application

O.I.P.E.

PATENT DATE

50 of
SCANNED

CCS Q.A. 088

— 2 —

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER		
09/900946	F	364	257	694	2895	827	Mitchell

APPlicants

Fumio Hirahara
Kenichi Ogata

TITLE

Semiconductor device having at least three power terminals superposed on each other

PTO-2040
12/99

ISSUING CLASSIFICATION

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ <small>(Assistant Examiner) _____ (Date)</small>			NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____				ISSUE FEE	
				Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ <small>(Primary Examiner) _____ (Date)</small> <small>(Legal Instruments Examiner) _____ (Date)</small>			ISSUE BATCH NUMBER	

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